


TSQ4-F22JF3C Optical Transceiver

Single-Mode 100GBASE-LR4 Transceiver, With Diagnostic Monitoring

Duplex QSFP28 100G OTU4 4I1-9D1F 10Km Transceiver

Features

- Supports 112Gbps
- I2C interface with integrated Digital Diagnostic Monitoring
- 4*28Gbps DFB-based LAN-WDM transmitter
- PIN and TIA array on the receiver side
- 4x28G electrical interface
- Single 3.3V DC Power Supply and Power Dissipation of 4W
- Distance up to 10km over SMF
- Duplex LC receptacles
- Operating case temperature: 0°C~+70°C
- RoHS Compliant 

Applications

- OTU4 4I1-9D1F

Production Description

The module is designed to support a maximum of four parallel channels of 28Gb/s optical signals and is specifically designed to meet OTU4 4I1-9D1F requirements specified in ITU-T Recommendations G.959.1/G.709 and Supplement 39(G_sup39). The module converts 4 input channels of 28Gb/s electrical data to 4 channels of LAN WDM optical signals and then multiplexes them into a single channel for 100Gb/s optical transmission. Reversely on the receiver side, the module de-multiplexes a 100Gb/s optical input into 4 channels of LAN WDM optical signals and then converts them to 4 channels of 28Gb/s electrical data.

The central wavelengths of the 4 LAN WDM channels are 1295.56, 1300.05, 1304.58 and 1309.14 nm as per channels of the LAN WDM wavelength grid defined in IEEE 802.3ba. The high performance cooled LAN WDM DFB transmitters and high sensitivity PIN receivers provide superior performance.

The product is designed with form factor, optical/electrical connection and digital diagnostic interface according to the QSFP+ Multi-Source Agreement (MSA). It has been designed to meet the harshest external operating conditions of large temperature, humidity and EMI Interference.

Absolute Maximum Ratings

These values represent the damage threshold of the module. Stress in excess of any of the individual Absolute Maximum Ratings can cause immediate catastrophic damage to the module even if all other parameters are within Recommended Operating Conditions.

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	VCC	-0.5	+3.6	V
Storage Temperature	Tc	-40	+85	°C
Relative Humidity	RH	0	85	%

Recommended Operating Environment

Parameter	Symbol	Min	Typical	Max	Unit
Power Supply Voltage	VCC	3.15	3.30	3.45	V
Operating Case Temperature	Tca	0		70	°C

Electrical Characteristics

Parameter	Symbol	Min.	Typical	Max	Unit	Ref.
Transmitter						
Input differential impedance	Rin	-	100	-	Ω	1
Single-ended Input Voltage Tolerance		-0.3	-	4.0	V	
AC Common Mode Input Voltage Tolerance		15		-	mV	
Differential Input Voltage		50	-	-	mV	
Differential Input Voltage swing, per lane	Vin	190		1000	mV	
Receiver						
Output differential impedance	Rout		100		Ω	1
Differential Output Swing, per lane	Vout	300		900	mV	2
AC Common Mode Output Voltage Tolerance				7.5	mV	
Single-ended Output Voltage		-0.3		4.0	V	

Notes:

[1] AC coupled.

[2] Into 100 ohm differential termination.

Transmitter Specifications – Optical

Parameter	Symbol	Min	Typical	Max	Unit	Ref.
Bit Rate per Lane	DR	27.9525 ± 20ppm			Gb/s	1
Data Rate Variation		-20		20	ppm	
Lane_0 Center Wavelength	λ0	1294.53	1295.56	1296.59	nm	
Lane_1 Center Wavelength	λ1	1299.02	1300.05	1301.09	nm	
Lane_2 Center Wavelength	λ2	1303.54	1304.58	1305.63	nm	

Information and specifications are subject to change without notice.
Please visit www.china-tscom.com for more information

8 Jinxiu Middle Road, Pingshan, Shenzhen, Guangdong, 518118, P. R. China
+86 755 32983688 | info@china-tscom.com | www.china-tscom.com

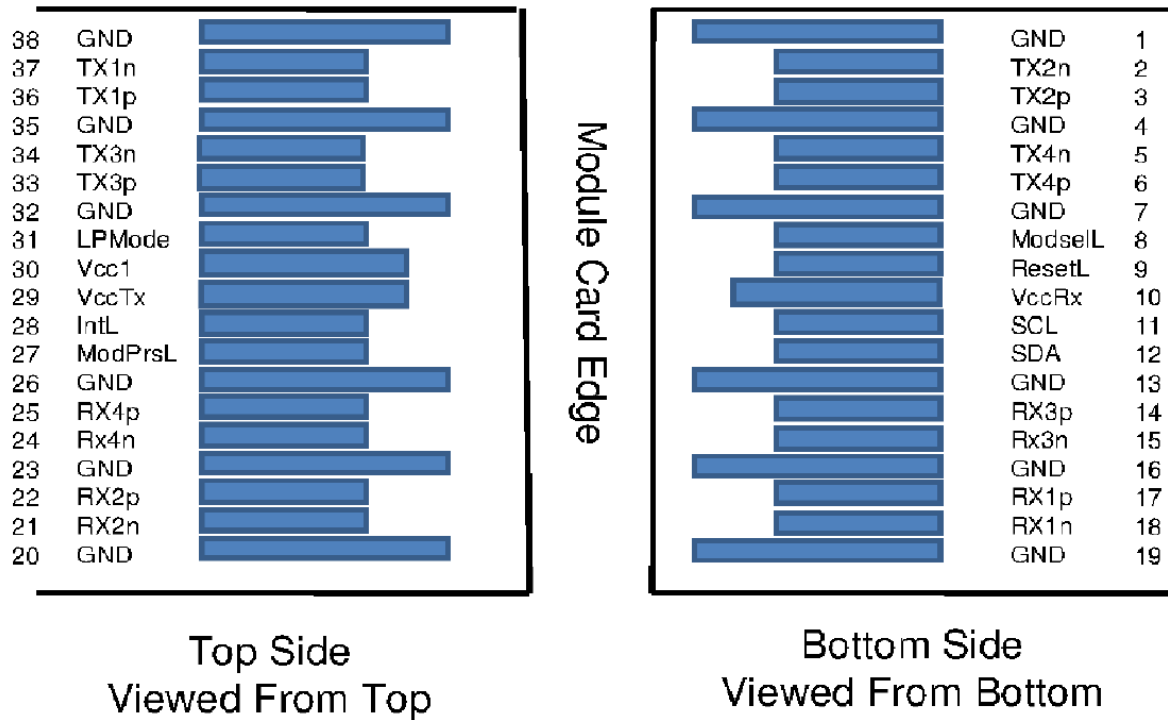


Lane_3 Center Wavelength	λ_3	1308.09	1309.14	1310.19	nm	
Total Average Output Power	Po			10.5	dBm	
Average Launch Power each Lane		-0.6		4	dBm	2,6
Difference in launch power between any two lanes (OMA)				5	dB	
Extinction Ratio	ER	4			dB	
Side-mode Suppression ratio	SMSR	30			dB	
Average launch power of OFF transmitter per lane	Poff			-30	dBm	
Transmitter Reflectance				-12	dB	
Optical Return Loss Tolerance	ORL			20	dB	
Transmitter eye mask definition {X1, X2, X3, Y1, Y2, Y3}		{0.25, 0.4, 0.45, 0.25, 0.28, 0.4}				3

Receiver Specifications – Optical

Parameter	Symbol	Min	Typical	Max	Unit	Ref.
Bit Rate per Channel	DR	27.9525 ± 20ppm			Gb/s	4
Data Rate Variation		-20		20	ppm	
Lane_0 Center Wavelength	λ_0	1294.53				

Qsfp28 Transceiver Electrical Pad Layout



Pin Definition

Pin	Symbol	Name/Description
1	GND	Ground
2	Tx2n	Transmitter Inverted Data Input
3	Tx2p	Transmitter Non-Inverted Data Input
4	GND	Ground
5	Tx4n	Transmitter Inverted Data Input
6	Tx4p	Transmitter Non-Inverted Data Input
7	GND	Ground
8	ModSelL	Module Select
9	ResetL	Module Reset
10	VCC Rx	+3.3 V Power supply receiver
11	SCL	2-wire serial interface clock
12	SDA	2-wire serial interface data
13	GND	Ground
14	Rx3p	Receiver Non-Inverted Data Output
15	Rx3n	Receiver Inverted Data Output
16	GND	Ground
17	Rx1p	Receiver Non-Inverted Data Output
18	Rx1n	Receiver Inverted Data Output
19	GND	Ground
20	GND	Ground
21	Rx2n	Receiver Inverted Data Output
22	Rx2p	Receiver Non-Inverted Data Output

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23	GND	Ground
24	Rx4n	Receiver Inverted Data Output
25	Rx4p	Receiver Non-Inverted Data Output
26	GND	Ground
27	ModPrsL	